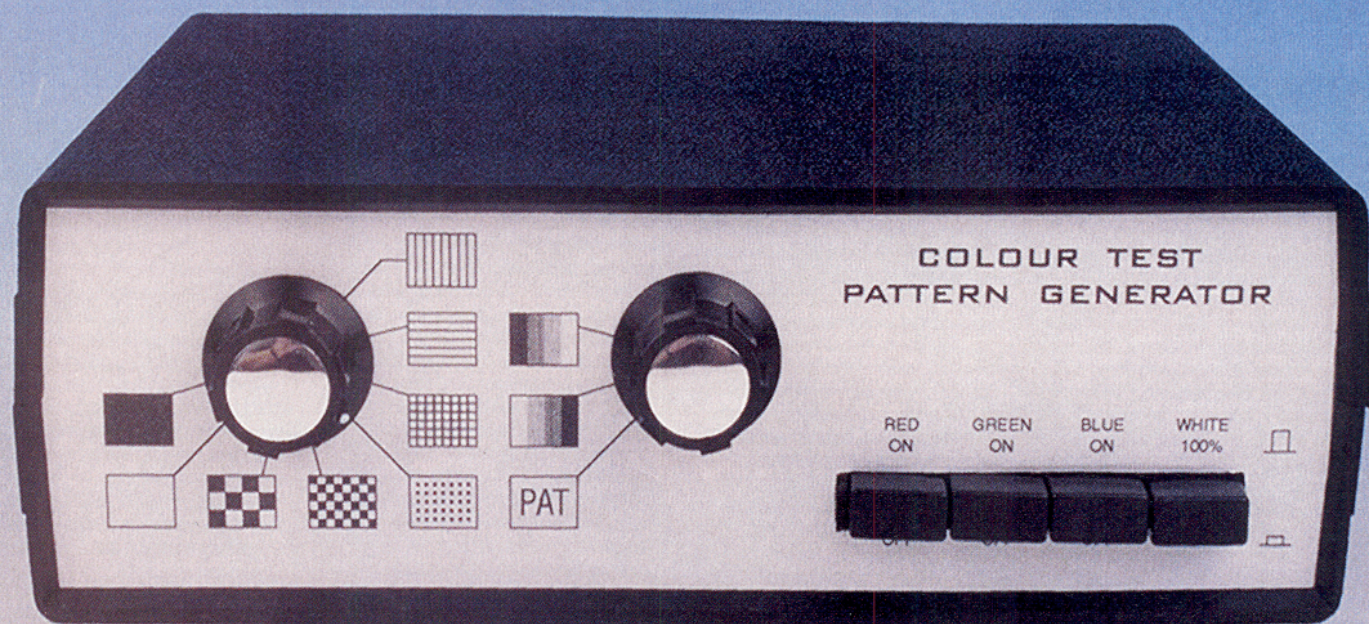


COLOUR TEST
PATTERN GENERATOR

When servicing and repairing televisions and other items of video equipment, it can be helpful to have a stable and recognisable test signal.

A colour bar pattern is often used for this, and indeed, many television and video recorder service sheets use this type of signal as a reference.

Colour TEST PATTERN GENERATOR

Design and Text by Paul Stenning

A variety of other patterns are used for specific tests. For example, a crosshatch is useful for checking picture linearity and convergence (colour gun alignment), while a checkerboard pattern or dots are ideal for setting up the focus.

The test pattern generator described here features these patterns as well as horizontal lines, vertical lines, dots, blank raster and white screen. The three primary colours are

individually switchable. So, for example, the white screen can be switched to red, green or blue for checking colour purity.

There have been several designs for test pattern generators published in the past. Many of these are monochrome only and are often based on the ZNA234E IC. Published designs for colour units are often complicated, and therefore, probably too expensive for occasional home use.

The aim of this design was to produce a straightforward, reliable piece of equipment with no unnecessary frills and features. It should be possible to construct the unit for around £100 including the case, which is much cheaper than commercial units having a similar specification. Setting up is straightforward and requires no test equipment.

This unit is an improved and updated version of a project by the same author, which was

published in the March 1994 edition of *Electronics in Action* magazine. The original design was one of the most popular projects in the magazine, with over one hundred PCBs being sold. Since *Electronics in Action* is, sadly, no longer being published, and a couple of the components used in the original design are now obsolete, it was felt that now is the right time to publish this revised version.

A major improvement with this version is that construction has been simplified. The circuit is built on two PCBs, and requires no interwiring. The white level can now be switched to either 75% or 100%, so that the colour bar pattern can give a proper white reference level. The original Astec modulator is no longer available, so this unit uses an Alps modulator from Maplin. Since this modulator has sound capabilities, a 1kHz audio tone generator has been added. Other modifications have been carried out to improve performance and reliability.

The instrument is mains powered. A standard composite video output is available (1V Pk-to-Pk into 75Ω), as well as UHF modulated (UHF channel E36). An oscilloscope trigger output is also available, which is switchable to either line or frame sync, or to the audio oscillator.

Circuit Description

The circuit diagram is shown in Figure 1. Most of the complicated stuff is handled by IC1 and IC2, which helps to simplify the remainder of the design. IC1 (SAA1043) is a universal sync generator, whose timing is

controlled by XT1. In this design, it is configured for the UK standard 625-line interlaced PAL, and four outputs are used.

IC1 requires a supply voltage between 5.7 and 7.5V. The SAA1043 has been operated with a 5V supply in several published designs (including some Maplin projects and the original version of this design). However, experience has shown that a limited number of devices will not operate correctly at this voltage.

Since the current consumption of IC1 is very low (less than 1mA), it is powered by a 6V8 supply rail, which is derived from the 12V supply by a Zener diode (D3) and resistor (R14). The output signals from IC1 are reduced to suitable levels for the 5V logic circuits by means of resistors (R15 to R19) and diodes (D4-6 and 9).

A 5MHz clock signal is derived from XT1. This controls the first binary counter (IC3, 74HCT4040), which is reset at the end of each line by the CB (composite blanking) signal. 258 clock pulses are counted by IC3 between each blanking pulse.

A second binary counter (IC4, 74HCT4024) counts the 312 or 313 CB pulses within a frame. This counter is reset by

the VERT signal at the end of each frame. The fact that the counter has a capacity of only 128 is irrelevant in this case, since it merely wraps round.

The various patterns are derived from the outputs of these counters with some logic and switching. I will come back to this in a moment, once I've described the inputs required for IC2.

IC2 (TEA2000) is a colour

encoder IC. To function, it requires composite sync and composite blanking signals that are supplied by IC1. The colour inputs set the output colour. There are six such inputs, two for each primary colour. Taking the red inputs for example, if R0 is high, a dark red is obtained, if R1 is high a brighter red is obtained, while if R0 and R1 are high, a very bright red results. These equate to 50%, 75% and 100% brightness, respectively. The device can produce 64 different colours, although only nine are used in this design.

For the colour bars, the R1, G1 and B1 lines are controlled in the appropriate sequence. For the black to white pattern, this is the binary output from three lines on IC3, while for the white to black pattern, these three lines are inverted by three gates in IC8. Output Q9 of IC3 is connected to the three gates in IC8 to prevent a brief white bar appearing at the end of the white-to-black colour bars when the count on IC3 exceeds 255. Three sections of SW2 select either of these colour bar patterns, or connects the three lines to the monochrome pattern logic.

The R0, G0 and B0 inputs to IC2 are controlled by IC11 and IC8:D. If SW7 is set to the 100% position, the output of IC8:D will go high when R1, B1 and G1 are all high. This gives a 100% white level. If SW7 is in the 75% position, the output of IC8:D will remain low. Although a four input NAND gate would have been sufficient for IC11, the 74HC133 is the only 74HC series NAND (or AND) gate with more than three inputs in the Maplin catalogue.

SPECIFICATION

Test patterns available:

Vertical bars
Horizontal bars
Crosshatch
Dots
Fine checkerboard
Coarse checkerboard
White screen
Blank raster
8 Colour bars (white to black)
8 Colour bars (black to white)
Switchable 75% or 100%
Primary colours individually switchable
PAL (UK standard)
1V Pk-to-Pk into 75Ω
1kHz (nominal) sine wave
1.2V Pk-to-Pk
Adjustable E30-E39,
6MHz sound subcarrier
230V AC, 50Hz, 6VA
Main – 192 × 145mm
Front – 97 × 46mm
76 × 203 × 180mm
(including knobs, etc.)
850g (excluding leads)

White level:

Colour switching:

Video system:

Composite video output level:

Audio tone frequency:

Audio output level:

UHF output channel:

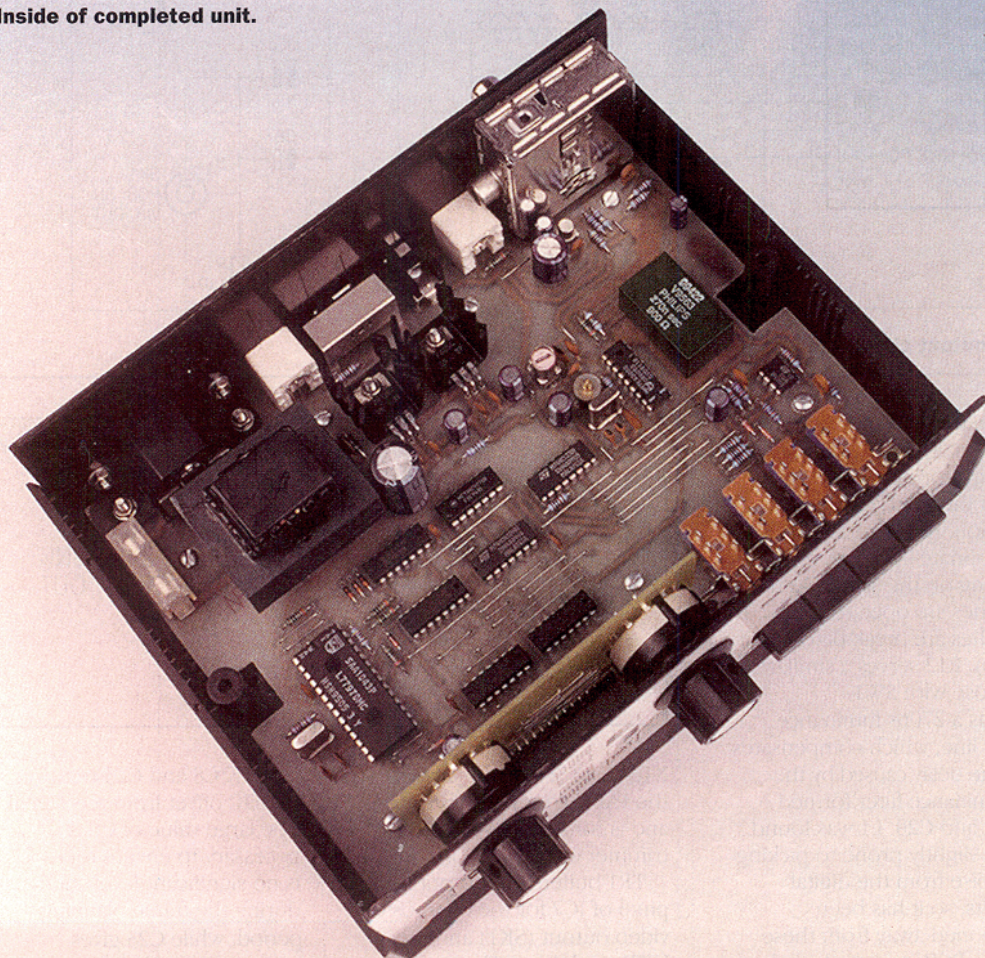
Power supply:

PCB sizes:

Overall size:

Weight:

Inside of completed unit.



With SW7 set to 100%, the white bar in the colour bar patterns will be brighter than the remaining colours (which are at 75%). This type of pattern is often specified in service manuals as the 100% white bar gives a true indication of the maximum video level, while the 75% colour levels are more representative of normal pictures. However, if the colour bar signal is, for example, being recorded to fill the end of a video tape, setting SW7 to 75% may give a more pleasing appearance.

For the monochrome patterns, the R1, B1 and G1 lines are taken high or low together, giving white or black. IC11 and IC8:D operate as previously, to give selectable white level. SW1 selects which pattern is displayed. The vertical bars are obtained from an OR gate on two outputs of IC3. The output of this OR gate is low

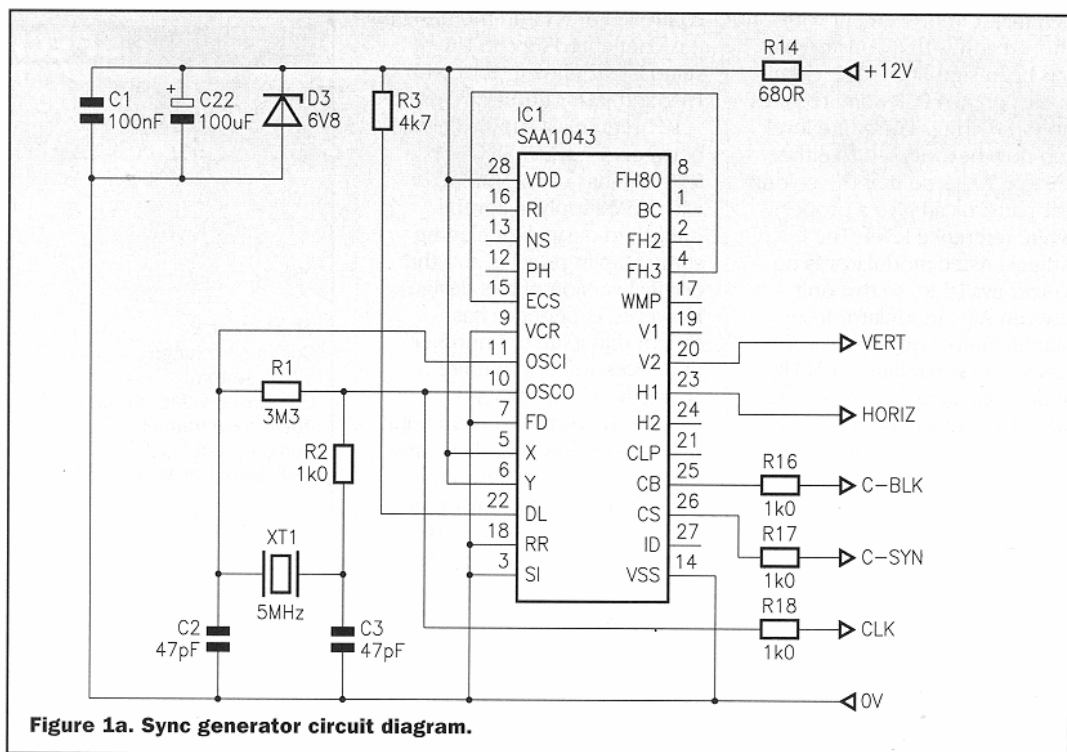


Figure 1a. Sync generator circuit diagram.

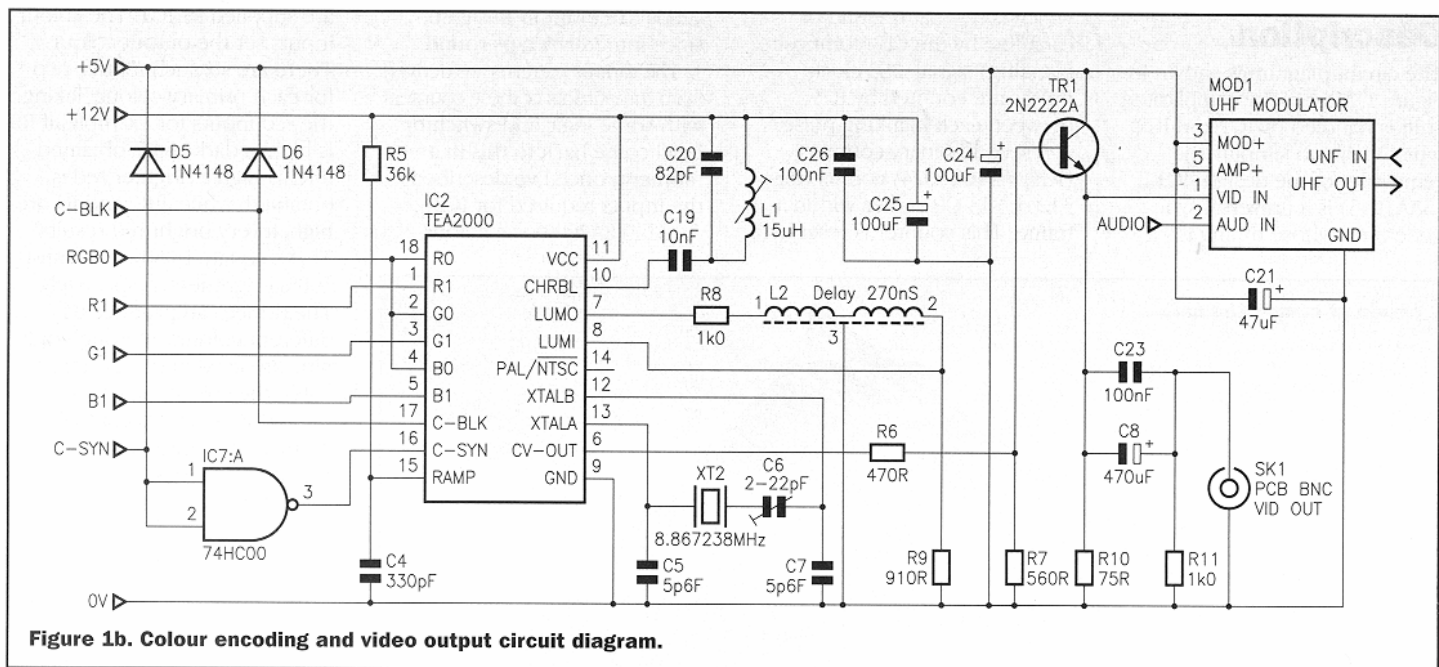


Figure 1b. Colour encoding and video output circuit diagram.

when the three lines are low, and this is inverted by the gate of IC6 after SW1. The horizontal lines are obtained by a similar arrangement on three outputs of IC4.

The crosshatch is obtained by an AND gate on the horizontal and vertical line signals, while the dot pattern is derived from an OR gate on these signals. The two checkerboard patterns are due to exclusive-OR gates on an output from each counter.

Two rotary switches (SW1 and SW2) were used for pattern selection, since the logic required to do the job with one switch would complicate the unit significantly, thereby adding to the cost. When SW2 is set to the "PAT" (pattern) position, the pattern selected by SW1 is displayed.

The three primary colours are individually switched by SW3-5. The appropriate colour is on when the switch is in the out position. R19-21 pull the appropriate lines low, to prevent flashing on the screen as the switches are operated (since the switches are break-before-make types). R12 serves a similar purpose with SW1.

L2 is a 270ns luminance delay line, which compensates for the delay caused by the chrominance filter formed by L1 and C20. L1 was found to be slightly prone to picking up noise from the digital circuits, so it has been positioned away from these on the PCB, and the board area beneath it is a ground plane.

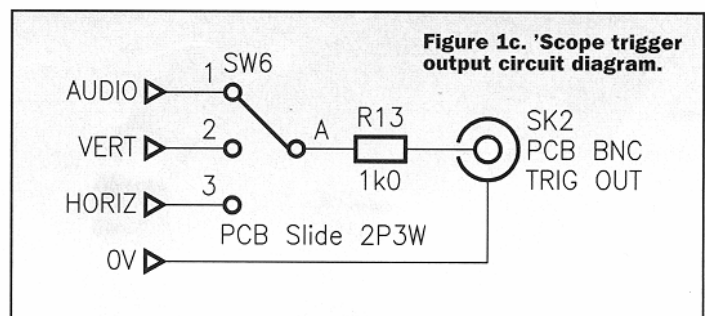
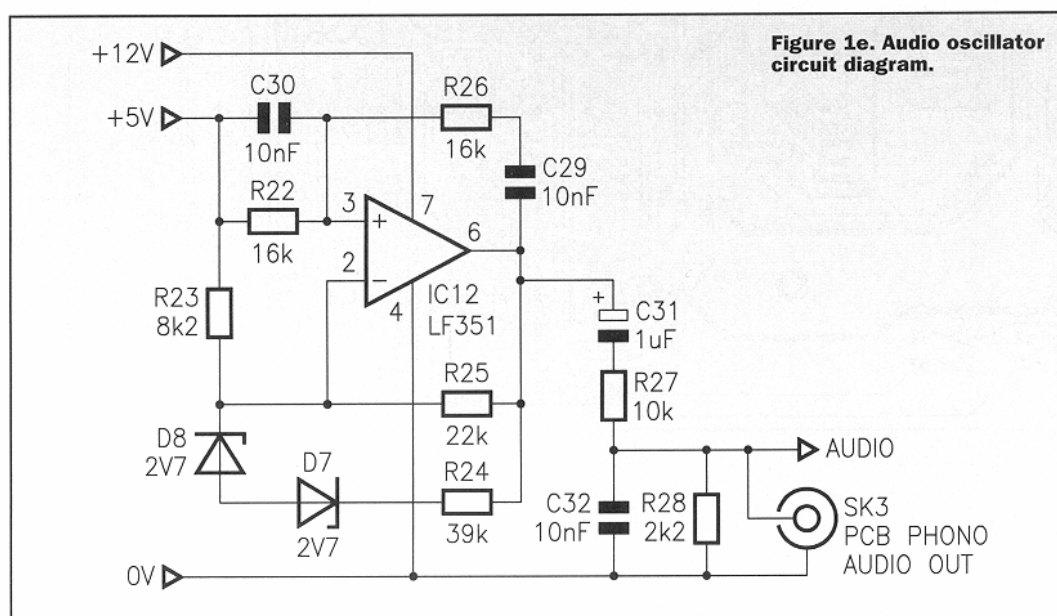
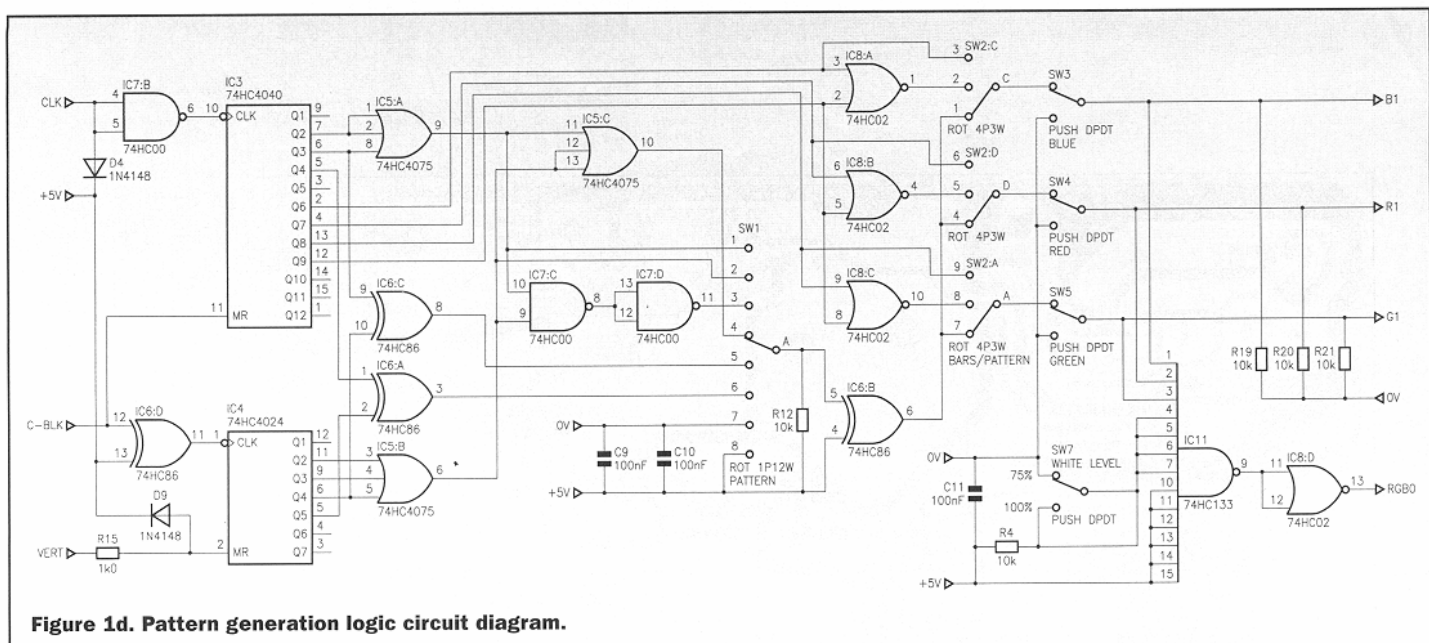


Figure 1c. Scope trigger output circuit diagram.

XT2 (8.867238MHz) is twice the PAL colour burst frequency, and is trimmed by a 2-22pF trimmer capacitor, C6.

TR1 buffers the output from pin 6 of IC2 for the composite video output (SK1) and the UHF modulator. The correct signal level is obtained by R6

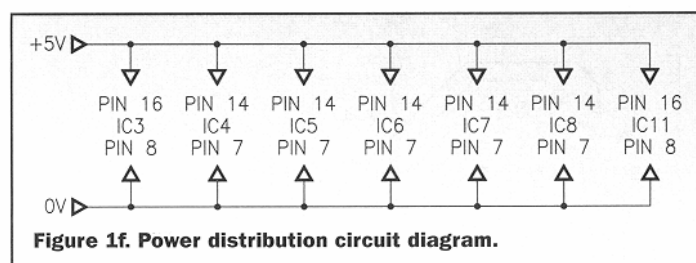
and R7. C8 and C23 remove the DC offset from this signal. The large value of C8 is necessary to ensure there is no significant level shift during the frame blanking period, while C23 gives improved coupling at high frequencies.



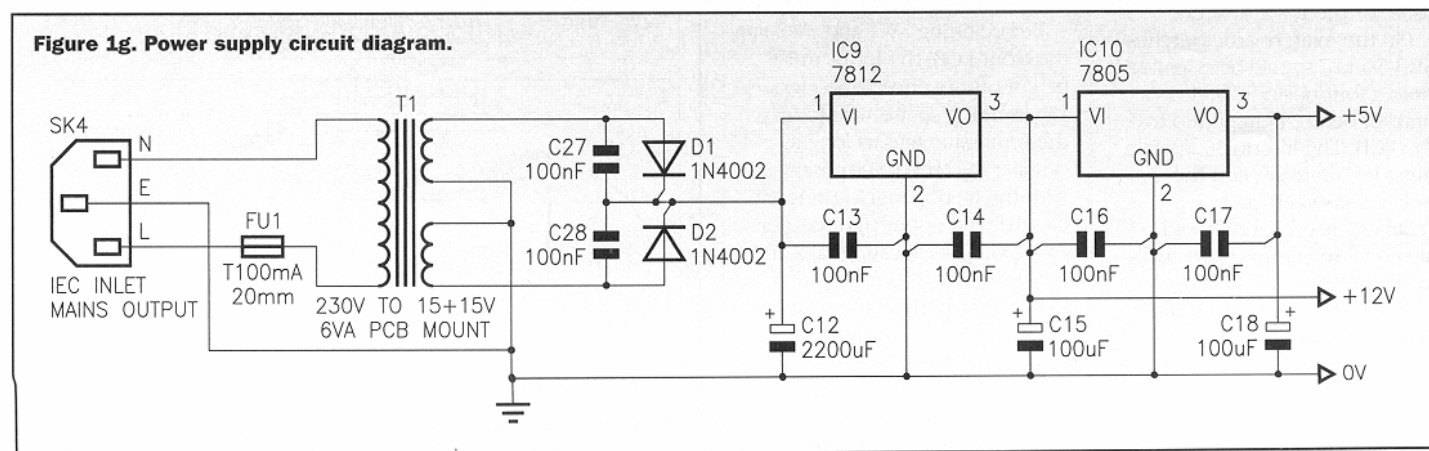
distortion is around 2%, which is adequate for this purpose. Note that the modulator itself has an audio distortion figure of 3%, so there is no point in providing it with a very low distortion signal. R27 and R28 reduce the audio signal to the correct level for the modulator. C32 removes the high frequency noise which emerges from the modulator audio input pin. The audio signal is also made available on SK3.

SK2 is intended for connecting to the trigger input of an oscilloscope, so that a stable trace may be obtained. SW6 selects the signal provided by this output, to either audio, video line or video frame.

The circuit requires 5V at about 180mA and 12V at about 70mA. Most of this current is consumed by the UHF modulator (160mA at 5V) and IC2 (60mA at 12V). The power is provided by a 6VA mains transformer and a pair of 78 series voltage regulators, which are mounted on small heatsinks. For safety, the 0V rail is earthed.



The circuit around IC12 is a Wien bridge oscillator, operating at 1kHz. Two Zener diodes are used to control the amplitude of the waveform, since they are much cheaper than a bead thermistor. The Zener diode arrangement only works with fixed frequency oscillators, such as this. The



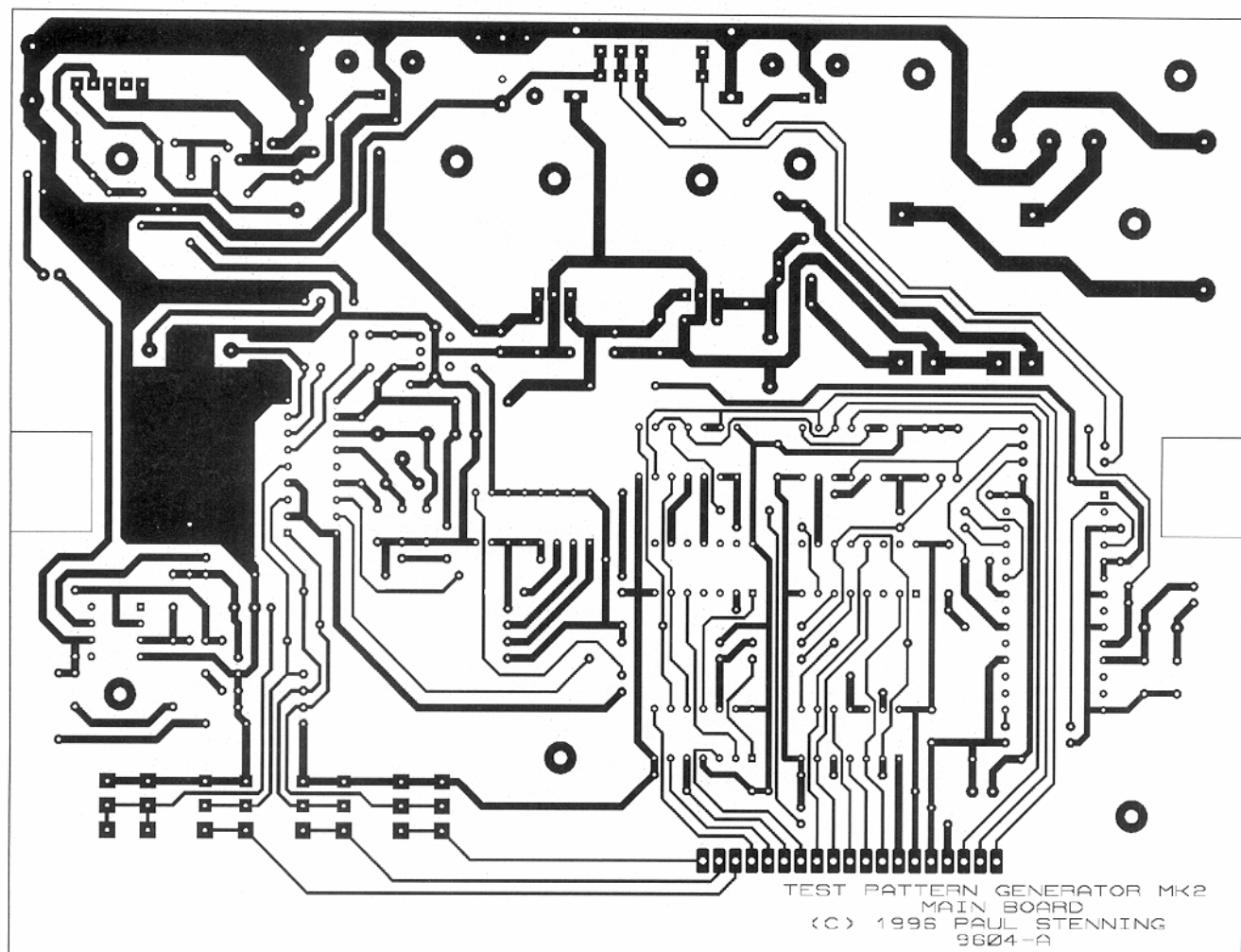


Figure 2a. Main PCB track.

PCB Construction

The circuit is assembled on two single-sided PCBs. The track layouts and component overlays are shown in Figures 2 and 3. Start with the wire links (use 24swg tinned copper wire), and then fit the components in the usual size order (starting with the smallest). Sockets may be used for the ICs if desired.

On the main board, switches SW3-5 and 7 should be assembled onto a four-position latch bracket before being fitted to the PCB. The interlocking plate must be removed, and the individual switch latches retained. The knobs should also be fitted at this stage to avoid stressing the PCB later.

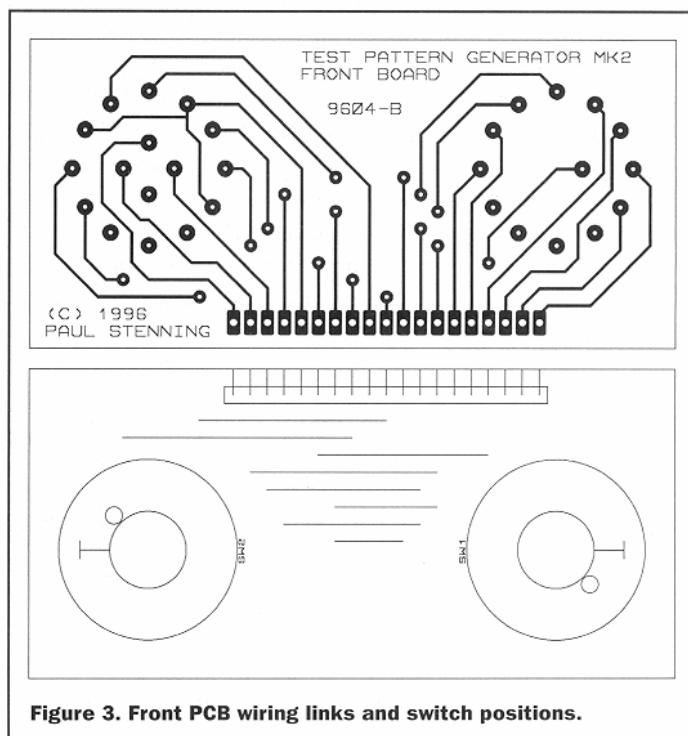
The voltage regulators (IC9 and IC10) must be fixed to small heatsinks and the PCB using M3 screws, nuts and washers. Although no electrical insulation is necessary, an

insulator between the device and heatsink will improve the thermal coupling.

SK1 must be fitted before the modulator, because of the (unused) phono socket on the side of the latter. SK4 (IEC mains inlet) should be fixed to the PCB with M3 hardware prior to soldering. The transformer should be fitted last, because of its size and weight.

Before fitting SW1 and SW2 to the front PCB, the loops must be cut off the end of the tags. When doing so, be sure to leave the remaining tags as long as possible. SW2 should be positioned with the front panel locating tab towards the upper right corner as shown. The locating tabs are not used, and should be cut close to the body of the switches.

The two PCBs are linked using a 19-way length of right-angled SIL header strip. This will need to be cut from a longer piece. The angled end of the pins



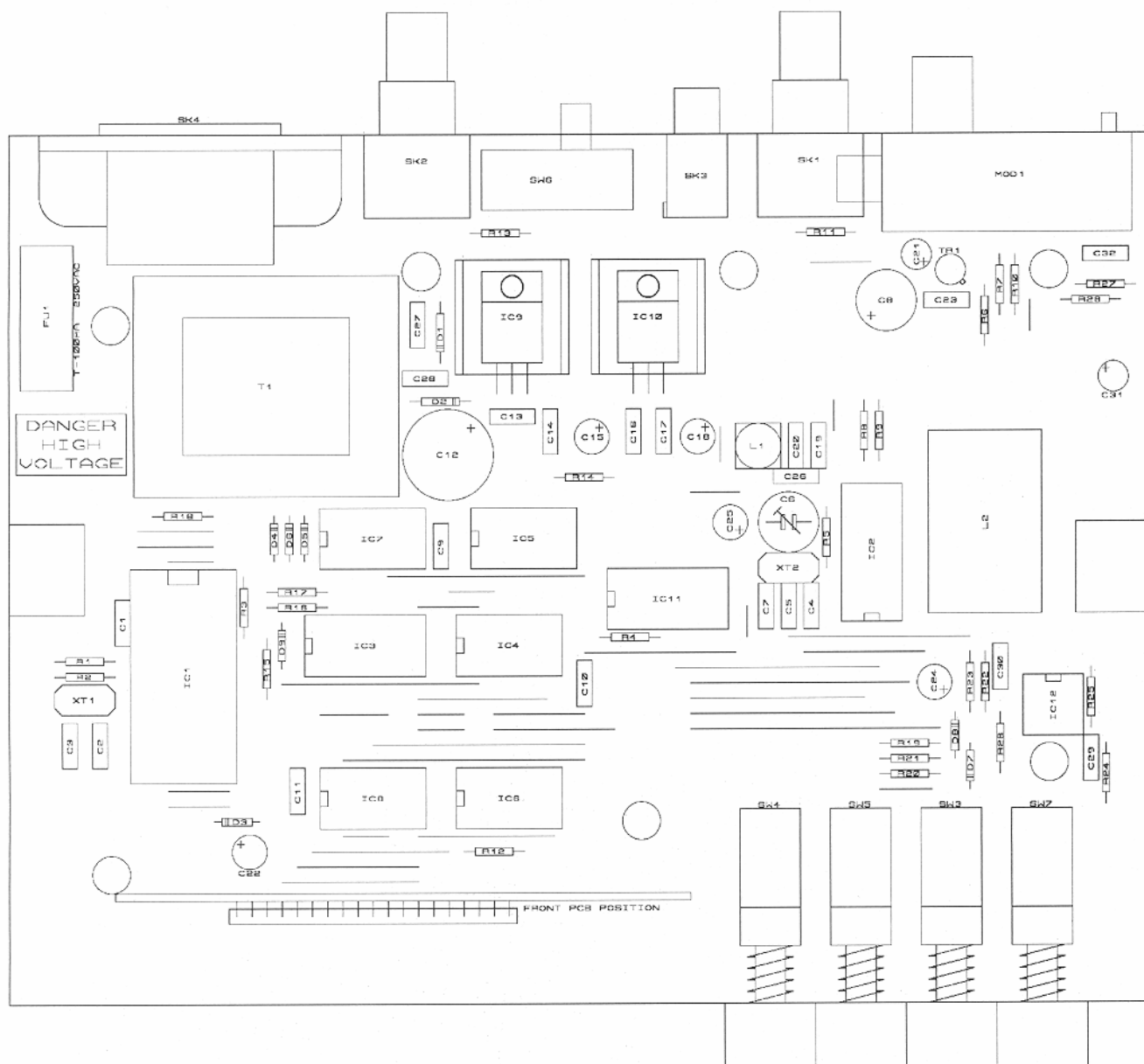
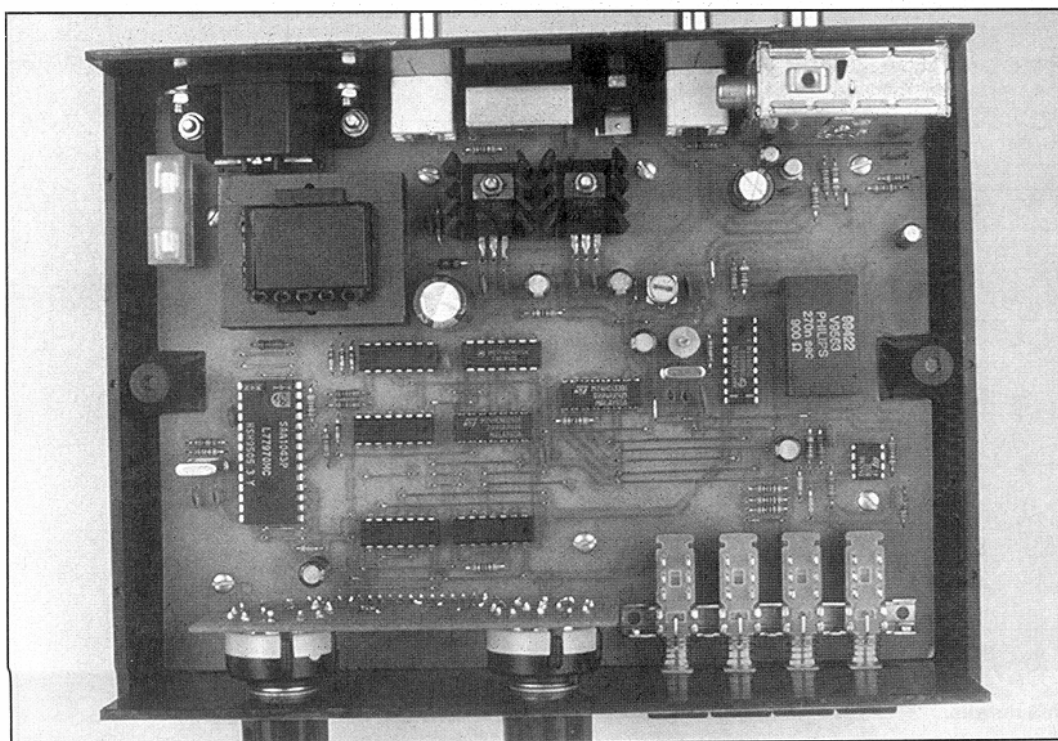


Figure 2b. Main PCB component positions.



must be fitted into the front PCB, so that the straight ends can then be fitted into the main PCB.

Case Assembly

The PCBs were designed to fit into the specified case. In the base of the case, there are two flat tags towards the back which must be removed.

There are also a number of PCB fixing posts, of which, only seven are used. The remaining posts must be shortened so that they do not foul on the underside of the PCB. This may be easily achieved using a sharp 8mm drill bit in a hand operated or SLOW electric drill (such as a rechargeable battery drill). Locate the tip of the drill bit in the hole at the top, and cut away each post until it is about half the original height.

Drilling details for the front and rear panels are shown in Figure 4. The rectangular holes

may be cut by drilling holes in two opposite corners and then cutting out the remainder using a fret saw. This can then be finished with a small file.

Suitable overlays for the front and rear panels are shown in Figure 5. These may be photocopied onto a white A4 self-adhesive label and fixed to the appropriate panels. For durability, they should then be covered with clear self-adhesive vinyl. The label and vinyl can then be trimmed around the holes using a sharp craft knife or scalpel. You may need to chamfer the inside edges of these panels slightly to allow them to fit into the case, because of the extra thickness on the outside.

Set the end stop of SW1 to eight positions and SW2 to three positions, and then fit the front panel to the switches. The shakeproof washers supplied with the switches should be positioned between the switches and the panel. Cut the shafts down to about 8mm, then fit the knobs.

The rear panel should be fixed to SK4 using M3 hardware. The supplied nuts should be used to fix the panel onto SK1 and SK2 (discard the shakeproof washers).

The whole assembly should then be slotted into the base of the case. Depending on the accuracy of your drilling, you may need to ease the mounting

holes in the PCB slightly, so that they coincide with the posts in the case. The PCB is held in place using seven 6.35mm long No.4 self tapping screws. Do not overtighten, otherwise the screws will strip the threads they have cut in the posts.

Testing and Setting Up

Providing the unit has been carefully constructed, there is no reason why it should not work first time. Connect the unit to the mains using an IEC mains lead. If a test meter is available, check the +12V and +5V rails are correct on the appropriate pins of the voltage regulators.

Take care, as there are mains voltages on the PCB. These are limited to the area around T1, FU1 and SK4, and are fairly inaccessible from above, providing the cover has been fitted to the fuse holder – but due care must still be taken.

Set the small slide switch on the back of the modulator to the upper position. Connect the UHF output to a television and tune in to UHF channel E36 (often used for video recorders and video games). You should be able to tune in two white vertical bars (this pattern is generated within the modulator). An audio tone should also be heard.

Set the switch on the modulator to the lower position. Ensure the four front panel push switches are out. Set SW2 fully clockwise and a set of colour (or monochrome) bars should appear. You may need to adjust the tuning on the television slightly.

Adjust C6 with a suitable trim-tool until the colour locks on. Adjust it back and forth to find the range of adjustment that gives a colour picture, then set it to the centre of this range.

Now adjust L1 for the clearest colours and a well defined edge where the colours change. This will probably be about a third of a turn up from the bottom. If an oscilloscope is available, this can be connected to the video output and L1 adjusted to peak the amplitude of the colour information on the signal.

If SW2 is turned to the centre position, the direction of the colour bar pattern should reverse. Operate SW7 (White), and the brightness of the white bar should reduce.

If SW2 is now set fully anticlockwise, the pattern displayed is controlled by SW1. Check SW1 works as expected, then set it to the white screen position and check SW3-5.

If the unit is left on for a while, the voltage regulators (IC9 and IC10), the TEA2000 (IC2) and the mains transformer

(T1) will become warm. You may also notice a faint smell of varnish from the transformer the first couple of times it warms up. This is normal, and should not cause concern.

Finally, fit the top cover of the case, with the two plastic tabs to the rear.

In Use

The UHF modulator has a pass-through facility, similar to that on video recorders. The unit may, therefore, be connected between an aerial and the equipment being tested, so that the normal channels may be obtained without changing connections. Due to the digital nature of the unit and the signals produced, it may cause some interference on the normal channels unless the signal from the aerial is fairly strong. If this occurs, you could try using an aerial amplifier between the aerial and the unit, otherwise, you will need to use the unit separately.

The modulator UHF output is tuned to channel E36 when supplied. An adjustment screw on the back allows this to be adjusted between channels E30 and E39. This adjustment may be necessary if you are using the unit in conjunction with a video recorder or if you live in an area where Channel 5

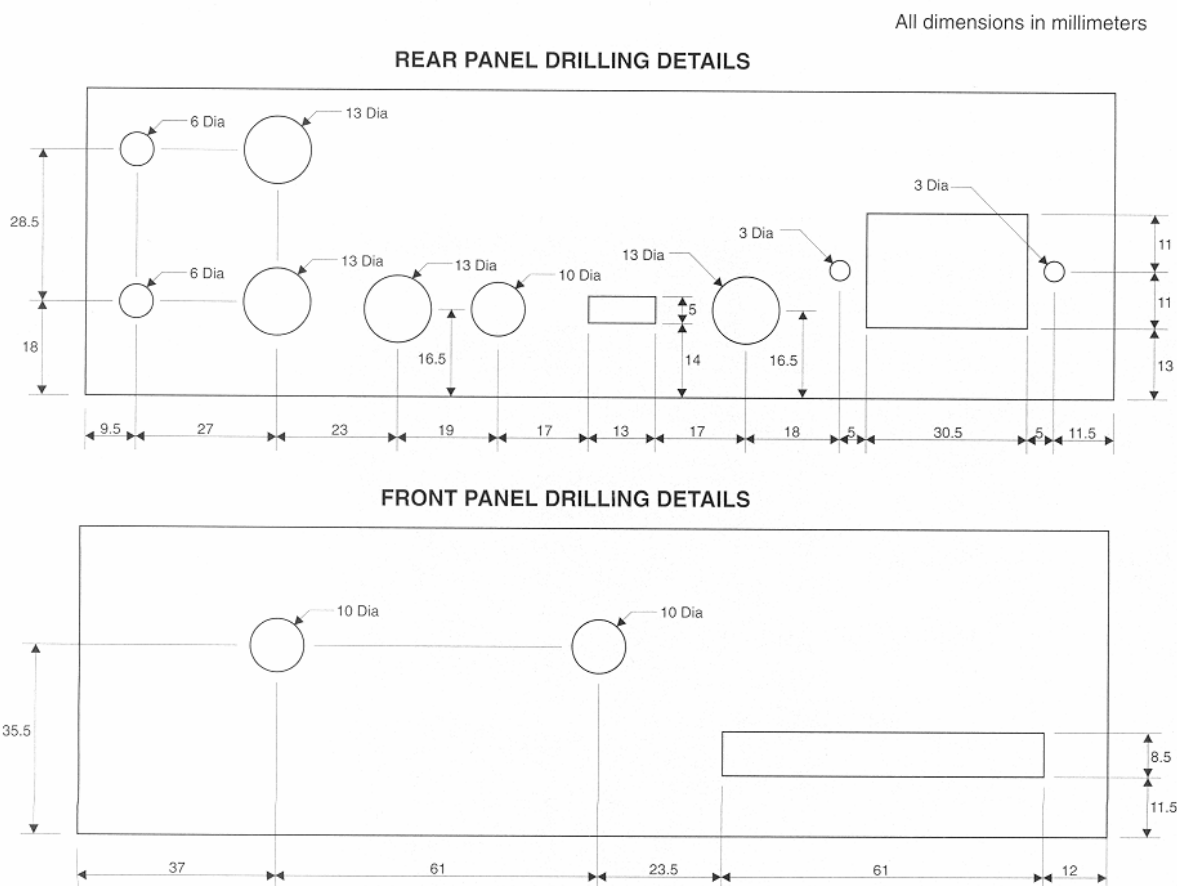


Figure 4. Front and rear panel drilling details.

is (or will be) broadcasting on channel E37.

The unit should be disconnected from the mains when not in use.

One final important comment – if you are working inside a television set or other piece of equipment with the power on, please be very careful.

Acknowledgements

The author would especially like to thank the former editor of *Electronics in Action* magazine, for granting permission to publish this design. He would also like to thank Maplin MPS for supplying the components used to construct the prototype.

Component Availability

The PCBs for this project are available as a pair from the author. Please write to Paul Stenning,

(enclosing an SAE or IRC) for prices.

All other components are available from Maplin MPS. Order Codes are given in the parts list.

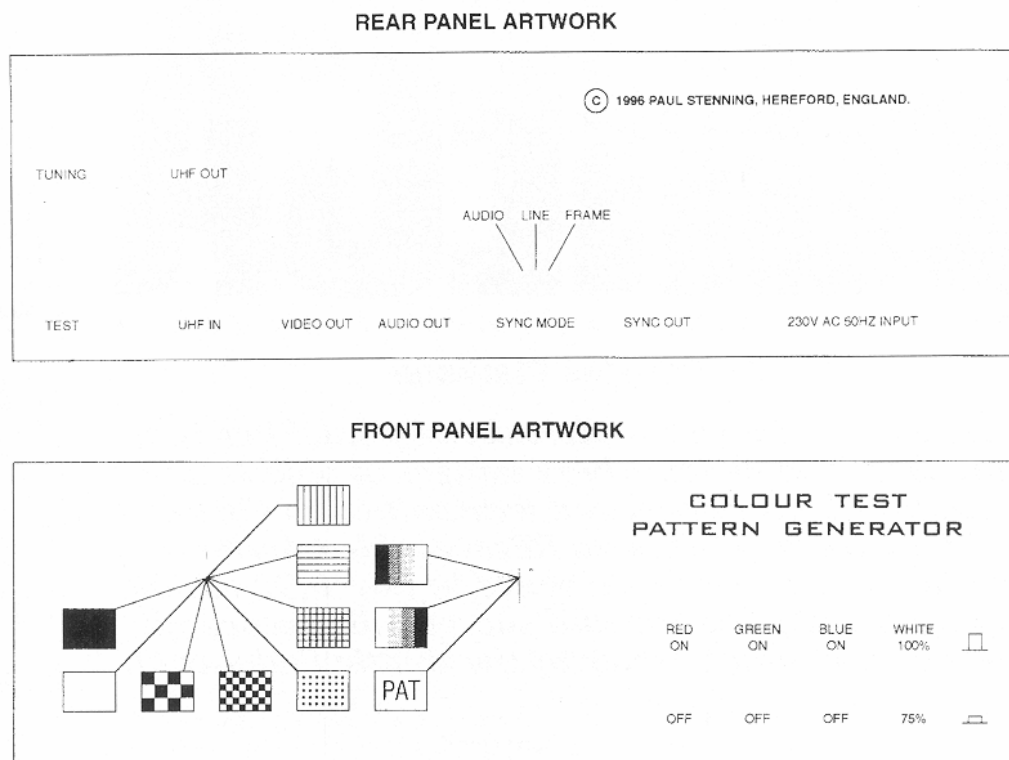


Figure 5. Front and rear panel artwork.

PROJECT PARTS LIST

RESISTORS: All 1% 0.6W Metal Film

R1	3M3	1	(M3M3)
R2,8,11,			
13,15-18	1k0	8	(M1K0)
R3	4k7	1	(M4K7)
R4,12,19,			
20,21,27	10k	6	(M10K)
R5	36k	1	(M36K)
R6	470Ω	1	(M470R)
R7	560Ω	1	(M560R)
R9	910Ω	1	(M910R)
R10	75Ω	1	(M75R)
R14	680Ω	1	(M680R)
R22,26	16k	1	(M16K)
R23	8k2	1	(M8K2)
R24	39k	1	(M39K)
R25	22k	1	(M22K)
R28	2k2	1	(M2K2)

CAPACITORS

C1,9-11,			
13,14,16,			
17,23,26-28	100nF Ceramic Disc	12	(BX03D)
C2,3	47pF Ceramic Disc	2	(WX52G)
C4	330pF Ceramic Disc	1	(WX62X)
C5,7	5p6F Ceramic Disc	2	(WX41U)
C6	2-22pF Trimmer	1	(WL70M)
C8	470μF 16V Radial Electrolytic	1	(AT43W)
C12	2,200μF 25V Radial Electrolytic	1	(AT53H)
C15,18,22,			
24,25	100μF 16V Radial Electrolytic	5	(AT40T)
C19,29,			
30,32	10nF Ceramic Disc	4	(WX77J)
C20	82pF Ceramic Disc	1	(WX55K)
C21	47μF 16V Radial Electrolytic	1	(AT39N)
C31	1μF 63V Radial Electrolytic	1	(AT74R)

SEMICONDUCTORS

IC1	SAA1043	1	(UK85G)
IC2	TEA2000	1	(UH66W)
IC3	74HC4040	1	(UF02C)
IC4	74HC4024	1	(UF01B)
IC5	74HC4075	1	(UF11M)
IC6	74HC86	1	(UB23A)
IC7	74HC00	1	(UB00A)
IC8	74HC02	1	(UB01B)
IC9	7812	1	(CH36P)

IC10	7805	1	(CH35Q)
IC11	74HC133	1	(UH30H)
IC12	LF351	1	(WQ30H)
TR1	2N2222A	1	(UH54J)
D1,2	1N4002	2	(QL74R)
D3	6V8 500mW Zener	1	(QH10L)
D4,5,6,9	1N4148	4	(QL80B)
D7,8	2V7 500mW Zener	2	(QH00A)

MISCELLANEOUS

SW1	1P12W Rotary	1	(FF73Q)
SW2	4P3W Rotary	1	(FF76H)
SW3-5,7	DPDT PCB Push	4	(FH67X)
SW6	2P3W PCB Slide	1	(FV02C)
SK1,2	PCB-mounted BNC Socket	2	(KE15R)
SK3	PCB-mounted Phono Socket	1	(HF99H)
SK4	PCB-mounted IEC Inlet Socket	1	(FE15R)
FU1	20mm T100mA Fuse	1	(UJ92A)
FU1	20mm PCB Fuseholder	1	(DA61R)
FU1	20mm Fuseholder Cover	1	(DA62S)
MOD1	UHF Modulator	1	(WC20W)
T1	230V to 15-0-15V 6VA		
	PCB-mounted Transformer	1	(YJ55K)
XT1	5MHz Crystal	1	(UL51F)
XT2	8-867238MHz Crystal	1	(UH85G)
L1	15μH Adjustable	1	(UH86T)
L2	270ns Delay Line	1	(UH84F)
	Case	1	(BZ76H)
	RF Flylead	1	(GW61R)
	Mains Lead	1	(MK41U)
	SIL Right-angled Header Strip	1	(JW60Q)
	4-way Latchbracket	1	(FH78K)
	Push Switch Knob	1	(FH61R)
	Knob	1	(RW90X)
	Heatsink	1	(FL58N)
	Insulator T0220	1	(QY45Y)
	24swg Copper Wire	1	(BL15R)
	6-35mm No.4 Self-tapping Screw	1	(FE68Y)
	M3 × 10mm Pan Head Screw	1	(JY22Y)
	M3 Nut	1	(JD61R)
	M3 Washer	1	(JD76H)
	Main PCB (9604-A)	1	
	Front PCB (9604-B)	1	

The Maplin 'Get-You-Working' Service is not available for this project.
The above items are not available as a kit.